

**Search Notes**

Application/Control No.

10/092,906

Examiner

Peter Coughlan

Applicant(s)/Patent under  
Reexamination

CHEN ET AL.

Art Unit

2129

**SEARCHED**

Class	Subclass	Date	Examiner
706	15	9/2/2005	PDC
706	45	9/2/2005	PDC
706	900	9/2/2005	PDC
375	354	9/27/2005	PDC

**INTERFERENCE SEARCHED**

Class	Subclass	Date	Examiner

**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
IEEE-Chen, Tseng, cup, speed, clock, neural, frequency, variable, change, radial basis function, gaussian, multiquadric,	9/2/2005	PDC
IEEE(cont)-clock multiplier, current, previous	9/2/2005	PDC
Google, Chen, Tseng	9/2/2005	PDC
ACM-cpu, speed, variable, neural network, clock, radial neural network, clock multiplier factor	9/2/2005	PDC
Inspec-cpu, speed, clock, frequency, neural, I	9/2/2005	PDC
EAST-frequency, clock, speed, Chen, Tseng, cpu, neural, radial, change, vector, variable, shew. baud, altering, clock multipl	9/2/2005	PDC
706/15-45-900 & 375/354 = ALPHA ALPHA with frequency, clock speed,	9/2/2005	PDC
ALPHA with ( frequency or 'clock speed'), (changing same frequency),cpu,	9/2/2005	PDC